

Curriculum Vitae

[Mendizabal, Jaizki]

CURRENT POSITION

Since 2017 – CEIT Head of Transport and Mobility Research group

Since 2009 - CEIT Head of Railways Signalling System Laboratory

PROFESSIONAL BACKGROUND

2015-2016 CEIT – Head of Communications Research Area

2011-2015 CEIT – Head of Embedded System Research line

2007-2011 CEIT – Project Manager

2005-2006 SANYO - Researcher

2002-2006 CEIT – PhD. Student

2000-2002 Fraunhofer Institute - Researcher

EDUCATION AND TRAININGS:

2006 – University of Navarra - PhD. in Communications Engineering

2005 – EU Japan Centre for Industrial Cooperation – Vulcanus Student

2000 – Fraunhofer Institut für Integrierte Schaltungen – Master Thesis

2000 – TU Wien – Erasmus Student

2000 – University of Navarra – Electronics Engineering

EXTRAS:

Dr. Mendizabal has participated in Shift2Rail as member of the Governing Board and leader of TD5.3

Dr. Mendizabal is co-author of a book related to GNSS integrated circuit design and to Railways Signalling Systems

Dr. Mendizabal has contributed to more than 30 publications in technical journals and international congresses